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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q68281

Hui ZHONG, et al.

Appln. No.: 10/049,270

Group Art Unit: 2811

Confirmation No.: 9445

Examiner: Tuan T. Dinh

Filed: February 11, 2002

For: MULTILAYERED PRINTED CIRCUIT BOARD, SOLDER RESIST COMPOSITION,
MULTILAYERED PRINTED CIRCUIT BOARD MANUFACTURING METHOD, AND
SEMICONDUCTOR DEVICE

PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of one month, extending the time for responding to the Office Action of October 6, 2004 to February 6, 2005.

A check for the statutory fee of \$120.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this sheet is enclosed.

02/02/2005 SDENB0B1 00000078 10049270

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120.00 OP

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860


WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: January 31, 2005

Respectfully submitted,


Patrick F. Gallagher
Registration No. 54,109